	Substitute for	form 1449	<b>VPTO</b>	Complete if Known		
INEC	DMATIO	N DIG	CLOSUBE	Application No.	not yet known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Filing Date	October 10, 2003	
				First Nam d Inv nt r	Jonathan S. McCalmont	
				Art Unit	not yet known	
			•	Examiner Name	not yet known	
Sheet	1	of	1	Attorney Docket Number	21901-07985	

			U.S. PAT	ENT DOCUMEN	TS	
		Document No.				
Examiner Initials*	Cite No.1	Number – Kind Code <sup>2</sup> (if known)	Date MM-DD-YYYY		Name of Patentee or Applicant of Cited Document	
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		Foreign Patent Document			
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		OTHER REFERENCES - NON-PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>8</sup>
MN		ALLEN & HOLBERG, CMOS Analog Circuit Design, Holt, Rinehart & Winston, New York, 1987, pp. 240-251	
MM	•	Gray & Meyer, <u>Analysis and Design of Analog Integrated Circuits</u> , 3d Ed., John Wiley & Sons, Inc. New York, 1993, pp. 317-333.	

21901/07985/DOCS/1383370.1

Examiner Signature Matthew V. Narryth Considered 6/15/05	
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<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609.

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